



Space product assurance

Relifing procedure – EEE components

**ECSS Secretariat
ESA-ESTEC
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Noordwijk, The Netherlands**

Foreword

This Standard is one of the series of ECSS Standards intended to be applied together for the management, engineering and product assurance in space projects and applications. ECSS is a cooperative effort of the European Space Agency, national space agencies and European industry associations for the purpose of developing and maintaining common standards. Requirements in this Standard are defined in terms of what shall be accomplished, rather than in terms of how to organize and perform the necessary work. This allows existing organizational structures and methods to be applied where they are effective, and for the structures and methods to evolve as necessary without rewriting the standards.

This Standard has been prepared by the ECSS-Q-60-14 Working Group, reviewed by the ECSS Executive Secretariat and approved by the ECSS Technical Authority.

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1 Scope

This standard specifies the requirements, also known as “relifing requirements”, for the planned, intentional storage, control, and removal from storage of electronic, electrical and electromechanical parts which are intended to be used for space applications.

The relifing process is a lot quality control activity. The inspections and tests defined do not constitute an up-screening or up-grading of components to a higher level of quality than procured to.

This standard is applicable to all EEE parts covered by ECSS-Q-ST-60 and used in space programmes.

This standard is not applicable to naked dice.

This standard does not cover the relifing of commercial parts.

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Normative references

The following normative documents contain provisions which, through reference in this text, constitute provisions of this ECSS Standard. For dated references, subsequent amendments to, or revision of any of these publications do not apply. However, parties to agreements based on this ECSS Standard are encouraged to investigate the possibility of applying the more recent editions of the normative documents indicated below. For undated references, the latest edition of the publication referred to applies.

ECSS-S-ST-00-01	ECSS system – Glossary of terms
ECSS-Q-ST-10-09	Space product assurance – Nonconformance control system
ECSS-Q-ST-60	Space product assurance – Electrical, electronic and electromechanical (EEE) components
ECSS-Q-ST-70-01	Space product assurance – Cleanliness and contamination control
ESCC 24900	Minimum Requirements for Controlling Environmental Contamination of Components
IPC/JEDEC J-STD-033 July 2002 version A	Standard for Handling, Packing, Shipping and Use of Moisture/ re-flow Sensitive Surface Mount Devices
ESCC 20600	Preservation, Packaging and dispatch of SCC Electronic Components

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Terms, definitions and abbreviated terms

3.1 Terms from other standards

For the purpose of this Standard, the terms and definitions from ECSS-S-ST-00-01 and ECSS-Q-ST-60 apply.

For the purposed of this Standard the following terms from ECSS-Q-ST-70-01 apply:

clean area

cleanroom

3.2 Terms and definitions specific to the present standard

3.2.1 antistatic material

material that minimizes the generation of static charges

NOTE 1 This term refers to the reduction of triboelectric charge generation.

NOTE 2 This property is not dependent upon material resistivity.

3.2.2 conductive material

<ESD protection> material with the following characteristics:

- surface conductive type: materials with a surface resistivity less than $10^5 \Omega/\square$.
- volume conductive type: materials with a volume resistivity less than $10^4 \Omega\text{-cm}$.

3.2.3 contaminant

unwanted molecular or particulate matter (including microbiological matter) on the surface or in the environment of interest that can affect or degrade the relevant performance or life time

3.2.4 container

receptacle which holds, restrains or encloses an item

3.2.5 (original) date code

code used by the EEE part manufacturer at assembly step that indicates the production date

NOTE 1 Generally four-figure codes; two for the year and two for the week.

NOTE 2 Special lot number can also identify the date code.

3.2.6 (relifing) date code:

code indicating the date an item is submitted to relifing

NOTE Four-figure code, two for the year and two for the week.

3.2.7 dissipative material

<ESD protection> material with the following characteristics:

- surface conductive type: materials with a surface resistivity equal to or greater than $10^5 \Omega/\square$ but less than $10^{12} \Omega/\square$.
- volume conductive type: materials with a volume resistivity equal to or greater than $10^4 \Omega\text{-cm}$ but less than $10^{11} \Omega\text{-cm}$.

3.2.8 electrostatic charge

negative or positive electrical charge present on the material or item surface, at rest

3.2.9 electrostatic discharge (ESD)

transfer of electrostatic charge between objects at different potentials caused by direct contact or induced by an electrostatic field

3.2.10 electrostatic discharge sensitive (ESDS)

tendency of the performance of EEE parts to be affected or damaged by an ESD event

3.2.11 ESD protected area

area which is constructed and equipped with the necessary ESD protective materials, equipment, and procedures, to limit ESD voltages below the sensitivity level of ESDS items handled therein

3.2.12 ESD protective material

material with one or more of the following properties: limits the generation of electrostatic charge, dissipates electrostatic charge, and provides shielding from electric fields

3.2.13 ESD protective packaging

packaging with ESD protective materials to prevent ESD damage to ESDS items

3.2.14 electrostatic shield

barrier or enclosure that prevents or attenuates the penetration of an electric field

3.2.15 handled or handling

actions during which items are hand manipulated or machine processed

3.2.16 identification

application of appropriate markings to ensure that the identity of an item is unfailingly indicated after preservation and each stage of packing

3.2.17 isolating material

<ESD protection> material not defined as conductive or dissipative are considered to be isolating

3.2.18 package

support used for enveloping, protecting or containing materials

NOTE Different types of packages are normally used:
Primary, intermediate and final packages

3.2.19 (primary) package

container, envelope or wrap holding an individual item

3.2.20 (intermediate) package

container holding two or more primary packages

3.2.21 (final) package

container holding one or more intermediate packages, used for transportation of supplies to the orderer

3.2.22 packaging

operations consisting in the preparation of supplies for transit and delivery.

NOTE The term includes preservation, identification and packing

3.2.23 packing

operation by which supplies are placed in container or wrapped and placed in containers

3.2.24 particle

unit of matter with observable length, width and thickness

NOTE A particle can be object of solid or liquid composition, or both, and generally between 0,001 μm and 1000 μm in size

3.2.25 preservation

cleaning of a item and the application of a suitable temporary protective, where necessary, to maintain the item in prime condition

3.2.26 relifing procedures

set of tests performed on an item previously stored to verify that its initial quality and reliability have not been affected by time

3.2.27 storage area

area in the storage site where EEE parts are stored and which contains one or more storage zones.

3.2.28 storage long duration

storage for which duration exceeds 3 years

3.2.29 storage site

geographical location where EEE parts are stored for a short, medium or long term period

NOTE For this site the requirements given in this standard apply: EEE parts manufacturer's premises, procurement Agency, EEE part user.

3.2.30 storage zone

defined space in which EEE parts are stored and which is equipped for the monitoring and the control of storage conditions.

3.2.31 timing parameters

One of the following parameters:

- T0 : Original date code
- T1 : Maximum allowed storage period from T0 with no relifing control
- T2 : Maximum duration between the original date code of part and its mounting
- T3 : Maximum allowed storage period after a relifing control.

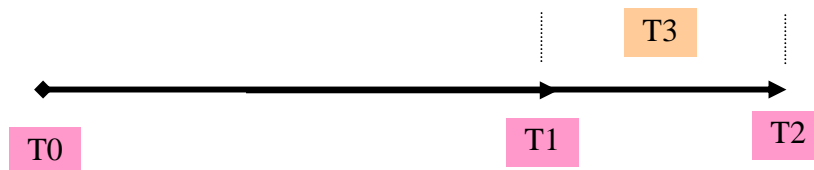


Figure 3-1: Timing parameters

3.2.32 triboelectric effect

generation of electrostatic charge on an object by rubbing or other type of contact.

3.3 Abbreviated terms

For the purpose of this Standard, the abbreviated terms from ECSS-S-ST-00-01 and the following apply:

Abbreviation	Meaning
ASIC	application specific integrated circuit
CCD	charge coupled device
CDM	charge device model
DPA	destructive physical analysis
DSP	digital signal processor
EEE	electronic, electrical and electromechanical
ESCC	European space components coordination
ESD	electrostatic discharge
FPGA	field programmable gate arrays
HBM	human body model
JEDEC	joint electronic devices engineering council
MM	machine model
MMIC	microwave monolithic integrated circuit
NA	not applicable
NCR	nonconformance report
NSA	national space agency
RH	relative humidity
SCSB	space components steering board
VLSI	very large scale integration

3.4 Symbols

Ω	ohm
Ω/\square	ohm per square
$\Omega\text{-cm}$	ohm centimetre
μm	micrometer
V	volt

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Environmental parameters for storage

4.1 General rules and requirements

4.1.1 Introduction

This clause defines the rules for storing EEE parts used on space programs.

Those rules are in line with the requirements defined in ESCC Basic Specification 24900.

4.1.2 Procedures

- a. The following domains shall be covered and documented by procedures sent to the customer for information, on request:
 1. Storage area and storage zone
 2. Cleanliness
 3. ESD protection
 4. Packing and Packaging
 5. Handling
 6. Quality assurance.

4.1.3 Storage area and storage zone

- a. It shall be demonstrated that storage areas and storage zones provide such protection against vibration, electromechanical fields, radiation fields and against light so that possible degradation of organic packaging material is prevented.

4.1.4 Cleanliness

- a. Rules for cleanliness efficiency shall be implemented.
- b. The working areas and the contained equipment shall be maintained as visually clean with no loose material.
- c. Access rules shall apply for personnel, materials and equipment.
- d. Storage areas shall conform to a cleanliness level as defined in ECSS-Q-ST-70-01 clause 5.3.1.4.

NOTE This cleanliness level is often called and known as "grey zone".

4.1.5 ESD protection

- a. The efficiency of ESD protection measures in storage, handling and testing areas shall be demonstrated.

NOTE Guidelines in Annex B can be used for this demonstration.

4.1.6 Packing – Packaging – Handling

- a. ESCC 20600 or IPC/JEDEC J-STD-033 shall apply for packing, packaging and handling.

NOTE Best practice is to pay attention to ESD sensitivity as described in Annex B.

4.1.7 Quality assurance requirements for storage areas

- a. The storage responsible entity shall establish and document the following:
1. Prohibited materials
 2. Personnel access rules
 3. Prohibited personnel actions
 4. Measures and facilities to segregate and protect components during receiving, inspection, storage and delivery
 5. Control measures to ensure that electrostatic discharge susceptible components are identified and handled only by trained personnel using anti static packaging and tools.

4.2 Storage conditions

4.2.1 Air

Normal air is used.

4.2.2 Temperature

- a. Temperature in the immediate vicinity of stored components shall at all times be maintained between a minimum temperature of 17 °C and a maximum temperature of 27 °C.

NOTE This is to avoid chemical reactions catalysis when it is too high or electronic reactions on certain technologies when it is too low.

4.2.3 Relative humidity (RH)

- a. RH in store cupboards shall be kept in the range defined by ESCC 24900 section 7.

NOTE This is to avoid, when combined with temperature, corrosion phenomena. The lower is the Relative Humidity the greater is the probability for ESD damage.

4.2.4 Package

- a. The packages used during storage shall ensure protection against ESD as defined in clause 4.1.6 and against any form of corrosion or contamination.
- b. Parts shall be stored in packages such that it can be demonstrated that they offer protection against ESD, corrosion and contamination including the contamination induced by the package itself.

NOTE Their primary packages can be used as long as they meet this requirement.

- c. CCDs and opto-electronic sensors shall be stored in dry air or in neutral ambience, to prevent risks of cover glass pollution and moisture ingress.

5 Timing parameters

- a. When used, relifing shall be performed anywhere between T1 and T2 according to the timing parameters definition as given in clause 3.2.31 and specified in Table 5-1.

NOTE For parts not planned to be mounted and to be kept in stock, relifing is not mandatory.

Table 5-1: Timing parameters

	T1	T2	T3
All components (except savers)	7 years	10 years	3 years
Savers	10 years	NA	NA

- b. In case of parts procured through an external procurement entity, the customer should require the supplier to state the minimum period of validity of parts (w.r.t. T1 or T3) after delivery.

6

Control parameters

6.1 Test requirements

6.1.1 General requirements per EEE parts family:

- a. For relifing, the following tests, as specified in Table 6-1 shall be performed:
 1. External Visual Inspection
 2. Electrical measurements
 3. Seal test
 4. Specific test
- b. Component families not covered in Table 6-1 shall be subject to special procedures to be defined by the program.

NOTE The relifing procedure can be applied on a sub-lot containing only the quantity of components immediately needed for production. In this case, the relifing date-code is applicable only to parts actually tested. The time limits specified in Table 5-1 remain applicable for the residual sub-lot.

- c. When sampling is specified in Table 6-1, it shall be performed in accordance to the following criteria, with a minimum of 80 parts, calculated with respect to the number of relifed parts.
 1. < 80 parts : 100%, with 0 defect allowed
 2. 81 -> 280 parts : sampling is 80 parts, with 0 defect allowed
 3. > 280 parts : sampling is 80 parts, with 1 defect allowed
- d. The specifications and methods to be used during relifing shall be those that were in effect for the initial procurement or, if demonstrated that they are not applicable, the most recent updated issues.

6.1.2 Specific requirements per EEE parts family:

- a. Seal test shall be performed when applicable (hermetic cavity package).

NOTE See (1) in Table 6-1.
- b. For ceramic chip or moulded capacitors, electrical measurement shall be done after 4 hours of stabilisation at 125°C for Type II ceramic.

NOTE See (2) in Table 6-1.

- c. Multi-chips (staked) capacitors shall be submitted to 100% visual inspection and electrical testing.

NOTE See (3) in Table 6-1.

- d. For all type of tantalum capacitors (solid and non-solid), the following specific tests shall be performed before the parametrical measurement:

1. Apply 9 discharges and 8 charges with a cycle time of 2 seconds and under nominal voltage.
2. Perform a burn-in tests (96 hours, rated voltage, 85°C).
3. Monitor the current during both charge and discharge tests to detect short circuit.

NOTE See (4) in Table 6-1.

- e. For film capacitors using the polycarbonate technology, a DPA test shall be performed on three pieces ,including:

1. external visual inspection
2. sealing test after insulate sleeve removal
3. microsection on two pieces
4. decaping on third part.

NOTE See (5) in Table 6-1.

- f. For programmed parts, the total duration (storage and mission) shall not exceed data retention duration given by the manufacturer (when applicable).

NOTE See (6) in Table 6-1.

- g. For VLSI, hybrids and CCD, when electrical test is not practicable because of test program or product complexity, the validation may be transferred to use step (functional test, programming).

NOTE 1 See (7) in Table 6-1.

NOTE 2 For example, VLSI can be ASIC, FPGA, MMIC, DSP, microprocessors, microcontrollers.

- h. For electromagnetic relays of latching and non-latching type, 10 switching shall be run before electrical measurements.

NOTE See (8) in Table 6-1.

- i. For low frequency and radio-frequency wires and cables, at least 0.5 m shall be inspected and insulating material shall be removed on 0.2 m.

NOTE See (9) in Table 6-1.

- j. Electrical test shall be optional for cavity hermetically sealed qualified parts when the qualification level is in line with the quality level defined by the applicable annex: A-1, A-2 or A-3 of ECSS-Q-ST-60.

NOTE See (10) in Table 6-1.

6.1.3 Electrical testing

- a. The DC parameters as given in table 2 of the applicable ECSS detail specification (or equivalent in another specification system) shall be measured.

NOTE 1 It is important to pay attention to the test and set up procedures which can have changed since the initial date code.

NOTE 2 Additional burn-in and drift calculation to be performed are only those specified in Table 6.1. of the applicable ECSS detail specification.

6.1.4 External visual inspection

- a. In case of doubt or anomaly regarding any surface contamination, one part shall be sampled in order to make a solderability test according to the applicable test method.
- b. The solderability test results shall be recorded in the relifing report.
- c. The part tested for solderability shall be considered destroyed.

6.1.5 Seal test

- a. The sealing tests shall be recorded as "pass" where the results meet the requirements of the original procurement specification. Sealing tests include fine leaks or gross leaks or both, depending on the applicable specification.
- b. The measurement values of leaks on non-conforming components shall be recorded in the relifing report.

6.2 Nonconformance

- a. ECSS-Q-ST-10-09 shall apply for the handling and processing of nonconformances.

NOTE The processing of nonconformances is identical for both relifing and normal procurement procedures.

- b. Any components not satisfying at least one of the requirements included in this standard shall be considered as not conform.
- c. When sampling test (as per Table 6-1), any batch of components failing the sampling rule defined in clause 6.1.1 shall be considered as not conform.
- d. In the cases specified in 6.2c, the test shall be performed on a 100% basis on the whole lot and the causes of the nonconformance shall be determined.
- e. In the case specified in 6.2c, the test shall be performed on a 100% basis on the whole lot and the causes of the nonconformance investigated and recorded in the relifing report.
- f. In case of 100% test (as per Table 6-1), any batch of components shall be declared as not conform when failing the following requirement:
 - (a) lot size \leq 100 parts : 0 defect allowed
 - (b) lot size $>$ 100 parts : 1 defect allowed

6.3 Relifing datecode

- a. The relifing date code shall correspond to the week code of the first test performed on the lot.
- b. This date code shall be assigned independent of the report conclusions.
- c. The relifing date code shall not be marked on the component and no other additional marking added.

6.4 Relifing report

- a. When relifing a component, a relifing report shall be established in conformance with the DRD in Annex A and sent, on request, to the customer for information.

6.5 Certificate of Conformity

- a. Once a batch is accepted, supported by a relifing report giving an "acceptable" decision or as a result of NCR processing, the original Certificate of Conformity shall be annotated with the relifing date code.
- b. The Certificate of Conformity shall be attached with the components during their delivery.
- c. Discarded batches shall be processed internally by the relevant reject system of the supplier.

Table 6-1: Control parameters and detailed application of categories

	External Visual Inspection	ELECTRICAL (10)	SEAL (1)	SPECIFIC TESTS
capacitors, chip, ceramic	sampling	sampling (2)	no	no
capacitors, moulded, ceramic	sampling (3)	sampling (2), (3)	no	no
capacitors, glass (CYR, ...)	100 %	100 %	no	no
capacitors, mica (HTxx, ...)	100 %	100 %	no	no
capacitors, chip, solid tantalum (TAJ, T495, CWR11, ...)	sampling	100 %	no	(4) charge/discharge 100 %
capacitors, leaded, solid tantalum (CSR, ...)	sampling	100 %	no	(4) charge/discharge 100 %
capacitors, leaded, non solid (tantalum, CLR79, ...)	100 %	100 %	no	(4) Charge /discharge 100 %
capacitors, film (CRH, CHS, PMxx, MKTS, ...)	sampling	100 %	no	DPA on 3p (5)
capacitors, variable	sampling	no	no	no
connectors, non filtered, rectangular	100 %	no	100 %	no



	External Visual Inspection	ELECTRICAL (10)	SEAL (1)	SPECIFIC TESTS
connectors, filtered, rectangular	100 %	100 %	100 %	no
connectors, non filtered, circular	100 %	no	100 %	no
connectors, filtered, circular	100 %	100 %	100 %	no
contacts & accessories	No	no	no	no
crystals	100 %	100 %	100 %	no
diodes	100 %	sampling	100 %	no
diodes, microwave	100 %	sampling	100 %	no
Filters	100 %	100 %	100 %	no
fuses, "cermet"	sampling	sampling	no	no
fuses, wire link	sampling	sampling	no	no
heaters, flexible	100 %	100 %	no	no
inductors, coils, moulded	sampling	sampling	no	no
inductors, coils, non moulded	sampling	sampling	no	no
integrated circuits	100 %	sampling (6), (7)	100 %	no
integrated circuits, microwave	100 %	sampling (7)	100 %	no
µwave passive parts (isolators, circulators)	100 %	sampling	no	no
µwave passive parts (power dividers, couplers)	100 %	sampling	no	no
µwave passive parts (attenuators, loads)	100 %	sampling	no	no
oscillators (hybrids)	100 %	100 %	100 %	no
relays, electromagnetic, latching and non-latching	100 %	100 % (8)	100 %	no
resistors, fixed, film (RNC, MBx xxxx, ...) (except RNC90)	sampling	100 %	no	no
resistors, high precision, fixed, metal foil (RNC90, ...)	sampling	100 %	no	no
resistors, network, thick and thin film	sampling	100 %	no	no
resistors, current sensing (RLV, ...)	sampling	100 %	no	no
resistors, power, fixed, wirewound (RWR, ...)	sampling	sampling	no	no
resistors, power, fixed, wirewound, chassis mounted (RER, ...)	sampling	sampling	no	no
resistors, precision, fixed, wirewound (RBR, ...)	sampling	100 %	no	no



	External Visual Inspection	ELECTRICAL (10)	SEAL (1)	SPECIFIC TESTS
resistors, fixed, film, high voltage (RHV, ...)	sampling	sampling	no	no
resistors, fixed, thick and thin film, chip	sampling	100 %	no	no
switches, electromechanical	100 %	100 %	100 %	no
switches, thermostatic	100 %	100 %	100 %	no
thermistors	100 %	100 %	no	no
transformers	sampling	100 %	no	no
transistors	100 %	sampling	100 %	no
transistors, microwave	100 %	sampling	100 %	no
wires and cables, low frequency	sampling (9)	no	no	no
cables, coaxial, radio frequency	sampling (9)	no	no	no
hybrids	100 %	100 % (7)	100 %	no
surface acoustic waves	100 %	100 %	100 %	no
charge coupled devices	100 %	100 % (7)	100 %	no
opto discrete devices (photodiodes, LED, phototransistors, optocouplers, ...)	100 %	100 %	100 %	no
NOTE: For applicable notes, see clause 6.1.2.				

Annex A (normative) Relifing report - DRD

A.1 DRD identification

A.1.1 Requirement identification and source document

This DRD is called from ECSS-Q-ST-60-14C requirement 6.4a.

A.1.2 Purpose and objective

The purpose of this document is:

- a. to give the detailed references of the lot tested
- b. to describe the relifing tests performed
- c. to give the results obtained
- d. to give the date of tests

A.2 Expected response

A.2.1 Scope and content

- a. The relifing report shall give the following generic information:
 1. part style
 2. detailed specification (with issue and variant)
 3. item identification by the supplier
 4. quantity stored
 5. original datecode
 6. date of storage
- b. For each test, the relifing report shall indicate:
 1. operator
 2. date of test
 3. quantity tested
 4. quantity rejected
 5. comments

- c. The relifing report shall include a conclusion (accepted / rejected).
- d. The relifing report shall indicate the new datecode (after relifing).

A.2.2 Special remarks

Figure A-1 shows a proposed template to be used for the relifing report.



Part Style:	
Detailed specification:	Issue: Var:
Item identification at User:	
Quantity Stored:	Date code: Date of Storage:
TESTS	RELIFING
1. External visual	
Operator	
Date	
Quantity tested	
Quantity rejected	
Comments	
2. Electrical tests	
Operator	
Date	
Quantity tested	
Quantity rejected	
Comments	
3. Hermeticity	
Operator	
Date	
Quantity tested	
Quantity rejected	
Comments	
4. DPA (if any)	
Operator	
Date	
Quantity tested	
Results	
DPA Report number	
5. Other tests	
Conclusion:	
Accepted / Rejected	
New date code	

Figure A-1: Example of a relifing traveller sheet

Annex B (informative)

ESD

B.1 Generalities and nature of static electricity

Static electricity is electrical charges at rest.

The electrical charges can be created by two events, which are:

- a. The transfer of electrons within a body resulting in polarization and a net overall charge of zero. The polarization is caused by Induction, that is the body enters an electrostatic field or lines of force around another charged body but without contacting the charged body
- b. The transfer of electrons from one body to another resulting in Conductive Charging and in net positive or negative charge. Triboelectric effects cause the conductive charging

B.2 ESD control program

- a. Scope of ESD control program
- b. Tasks, activities and procedures necessary to protect ESDS items
- c. Identification of organizations responsible for the tasks and activities
- d. Listings of directive or guidance documents in the ESD control program
- e. Description of ESD control requirements imposed on subcontractors and suppliers
- f. Listing of the specific ESD protection tools materials and equipment

B.3 ESD protections

B.3.1 Protective areas

ESD protective areas are required when handling ESDS parts, assemblies and equipment outside of their ESD protective covering or packaging.

Dispositions are taken to limit static voltage levels below the damage threshold of the most sensitive ESDS part.

Implementation of protective areas leads to requirements for adequate grounding procedures, personnel electrical safety, Tools materials and equipment specification, operating procedures and development of handling procedures.

The following document may be used as guideline: MIL-HDBK-263 appendix F.

B.3.2 Protective covering and packaging materials

Personnel	Wearing of ESD protective smocks or clothing footwear (list not exhaustive) when handling ESDS items
Work areas, protected areas	Use of conductive materials or dissipative materials
EEE parts packaging	Use and control of antistatic materials for primary packages like dry-packs, plastic rails or tubes. Use of dissipative materials for trays wheels

The following document can be used as guideline : MIL-HDBK-263 appendix I.

Materials are normally classified as conductive, dissipative and isolative. The classification depends upon the material resistivity. Isolative materials are not ESD protective and special attention is recommended.

Surface treated isolative material is used as antistatic materials if their resistivity is compliant to the definition of clause 3.2.1.

Warning: Antistatic materials loose their antistatic properties after a certain time due to the fact that only the surface is impregnated with charges. So, special attention is recommended to control it.

B.3.3 Handling

The following document can be used as guideline: MIL-HDBK-263 appendix H.

B.4 EEE parts sensitivity to ESD classification

Parts are classified function of their sensitivity to ESD and 3 main models: HBM model, MM model and CDM model (Table B-1)

HBM: The principal source of ESD damage is the human body

MM: The prime source of damage for the MM is a charge machine or device.

CDM; The prime source of damage for the CDM is the rapid discharge of a charged part



Table B-1: ESD classes

ESD MODEL	ESD CLASS: Sensitivity (S) versus Voltage range	
HBM	1	0 V < S < 1999 V
	2	2 V < S < 3999V
	3	4 V < S < 15999 V
MM	1	0 V < S < 100 V
	2	101 V < S < 200 V
	3	201 V < S < 400 V
	4	401 V < S < 800 V
	5	800 V < S
CDM	1	0 V < S < 124 V
	2	125 V < S < 249 V
	3	250 V < S < 499 V
	4	500 V < S < 999 V

Bibliography

ECSS-S-ST-00	ECSS system – Description, implementation and general requirement.
MIL-HDBK-263	Electrostatic Discharge Control Handbook for protection of Electronic Parts, Assemblies and Equipment (excluding Electrically Initiated Explosive Devices)